L Number	Hits	Search Text	DB	Time stamp
-	143383	(data or information) adj1 process$4	USPAT; US-PGPUB	2002/10/17 12:39
-	1	(data or information) adj1 process$4 same micro$1processor$1 same communicat$4 same electronic$3 same circuit$1 same inversion$1 adj10 bit$1	USPAT; US-PGPUB	2002/10/17 12:42
-	1	(data or information) adj1 process$4 same micro$1processor$1 same communicat$4 same inversion$1 adj10 bit$1	USPAT; US-PGPUB	2002/10/17 13:49
-	2667	(data or information) adj1 process$4 same micro$1processor$1 same communicat$4	USPAT; US-PGPUB	2002/10/17 14:16
-	13	(data or information) adj1 process$4 same micro$1processor$1 same communicat$4 same bit$1 same order	USPAT; US-PGPUB	2002/10/17 14:16
-	1901	(data or information) same transfer$4 same process$4 same micro$1processor$1 same communicat$4	USPAT; US-PGPUB	2002/10/17 14:23
-	21	(smart or ic) same card same (data or information) same transfer$4 same process$4 same micro$1processor$1 same communicat$4	USPAT; US-PGPUB	2002/10/17 16:02
-	19	(data or information) same transfer$4 same process$4 same micro$1processor$1 same communicat$4 same inver$4	USPAT; US-PGPUB	2002/10/17 16:07
-	19	(data or information) same transfer$4 same process$4 same micro$1processor$1 same communicat$4 same inver$4	USPAT; US-PGPUB	2002/10/17 14:33
-	1	(smart or ic) same card same (data or information) same transfer$4 same process$4 same micro$1processor$1 same communicat$4 adj5 (device$1 or unit$1)	USPAT; US-PGPUB	2002/10/17 16:04
-	4647	data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4	USPAT; US-PGPUB	2002/10/17 16:10
-	834	data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4 same bit$1	USPAT; US-PGPUB	2002/10/17 16:11
-	31	data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4 same bit$1 same inver$4	USPAT; US-PGPUB	2002/10/17 16:12
-	3	data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4 same bit$1 same inver$4 same card$1	USPAT; US-PGPUB	2002/10/17 16:20
-	6	data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4 same bit$1 same inver$4 same function$1	USPAT; US-PGPUB	2002/10/17 16:38
-	0	(smart or ic or electro$4) adj1 card$1 same data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4 adj1 (device$1 or unit$1) same bit$1 same function$1	USPAT; US-PGPUB	2002/10/17 16:42
-	0	(smart or ic or electro$4) adj1 card$1 same data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4 adj1 (device$1 or unit$1) same bit$1	USPAT; US-PGPUB	2002/10/17 16:45
-	0	(smart or ic or electro$4) same card$1 same data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4 adj1 (device$1 or unit$1) same bit$1	USPAT; US-PGPUB	2002/10/17 16:51
-	57	(smart or ic or electro$4) same card$1 same data same (tranfer$4 or transmi$4) same micro$1processor$1 same communicat$4 same (device$1 or unit$1)	USPAT; US-PGPUB	2002/10/17 17:00
-	2	(("6299069") or ("5978822")).PN.	USPAT; US-PGPUB	2002/10/18 17:57
-	4	(("6299069") or ("5978822") or ("6377356") or ("6023705")).PN.	USPAT; US-PGPUB	2002/10/18 17:58
-	0	("hardware same circuit same inver$4").PN.	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 07:42

-	1209	hardware same circuit same inver$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 07:42
-	27	hardware same circuit same inver$4 same data same card$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 09:41
-	6	card$1 same microprocessor same inver$4 same reader	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 09:58
-	125	card$1 same microprocessor same inver$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 16:20
-	12885	326/$.ccls.	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 13:20
-	1	(card$1 same microprocessor same inver$4) and 326/$.ccls.	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 13:27
-	7396	(inver$4 same circuit$1) and 326/$.ccls.	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 13:28
-	1717	235/492.ccls.	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 15:51
-	0	(235/492.ccls. and inverter) and (shift adj1 register) and filo	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 15:51
-	34	(235/492.ccls. and inverter) and (shift adj1 register)	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 17:44
-	1	4004133.pn. and inver$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 17:38
-	1	4004133.pn. and inver$4 and card and reader$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 17:39
-	0	card$1 same reader$1 same (microprocessor or micro-processor or (micro adj1 processor)) same inver$4 same bit$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 17:46
-	7	card$1 same reader$1 same (microprocessor or micro-processor or (micro adj1 processor)) same inver$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/04 17:46
-	2	(conventional adj1 signal$1) same processor same word same bits	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 12:45
-	2	((conventional adj1 signal$1) same processor same word same bits) same processor$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 12:46
-	0	inver$4 near10 order$1 near20 bits near20 word$1 same card$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 12:48
-	76	inver$4 near10 order$1 near20 bits near20 word$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 13:28

-	4	inver$4 near10 order$1 near20 bits near20 word$1 same (microprocessor or micro-processor or (micro adj1 processor) or processor)	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 13:23
-	1	4256926.pn. and order$1 and bit$1 and microprocessor$1 and card$1 and communication	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 13:24
-	2	inver$4 near10 order$1 near20 bits near20 word$1 same subscrib$3	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:00
-	1	6574776.pn. and processor$1 and read$3 and order$3 and word$1 and data and bit$1 and inver$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:22
-	6979	SIM or (subscriber near1 Identity near1 Module) same (microprocessor or micro-processor or (micro adj1 processor))	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:24
-	6979	SIM or (subscriber near1 Identity near1 Module) same (microprocessor or micro-processor or (micro adj1 processor)) same card$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:24
-	6978	SIM or (subscriber near1 Identity near1 Module) same (microprocessor or micro-processor or (micro adj1 processor)) same card$1 same inver$4 same order$4 same bit$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:26
-	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor)) same card$1 same inver$4 same order$4 same bit$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:26
-	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor)) same inver$4 same order$4 same bit$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:27
-	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor)) same inver$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:27
-	552	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor$1)	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:27
-	293	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor$1) same card$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:28
-	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor$1) same card$1 same inver$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:29
-	145	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor$1) same card$1 same communicat$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:29
-	95	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor$1) same card$1 same communicat$4 same (data or bit$1)	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 15:46
-	1	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor$1) same card$1 same communicat$4 same (data or bit$1) same order$4	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 14:31

-	0	(SIM or (subscriber near1 Identity near1 Module)) same (microprocessor or micro-processor or (micro adj1 processor) or processor$1) same card$1 same (right or left) same shift same register$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 15:46
-	1	5978822.pn. and left and right and switch$3 and register$1	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 15:53
-	109	235/492.ccls. and inverter	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 16:19
-	3	(card$1 same microprocessor same inver$4) and (235/492.ccls. and inverter)	USPAT; US-PGPUB; EPO; IBM_TDB	2003/06/06 16:20